

## UNIT I

### Fundamentals

\* Computer → does the human work & minimizes the workload of humans.  
- Electronic Machine (Hardware & Software)

1. I/p - users (I/p device - keyboard, Mouse)
2. Processing - CPU (Control Processing Unit) (Brain of computer).
3. O/p - Monitor, printer.

Programming language, Machine language.

### Computer Hardware

→ physical components.

→ Function efficiently & produce useful output only when h/w & s/w work together.

→ Internal Computer Hardware.

process/store the instructions delivered by the program/os.

1. Mother board
2. CPU
3. RAM
4. Hard drive
5. Solid state Drive
6. Optical Drive
7. GPU
8. NIC
9. Heat sink

- ① Mother Board → Central hub.  
holds the CPU & functions are carried out.
- ② CPU - brain of computer. Clock speed - measures the computer performance & efficiency.
- ③ RAM - Temporary Memory. Volatile Memory (Stored data is cleaned when computer is switched off).  
Information is immediately accessible to programs.
- ④ Hard drive - Store Temporary & permanent data.
- ⑤ SSD - Non-volatile, safe to store. Data remains permanent even when the computer is switched off.
- ⑥ GPU - Extension to CPU. Graphical Data.
- ⑦ NIC - Connect to Network / LAN / N/w Adapter.

### External Hardware Components.

→ peripheral components, controls either I/p or o/p functions

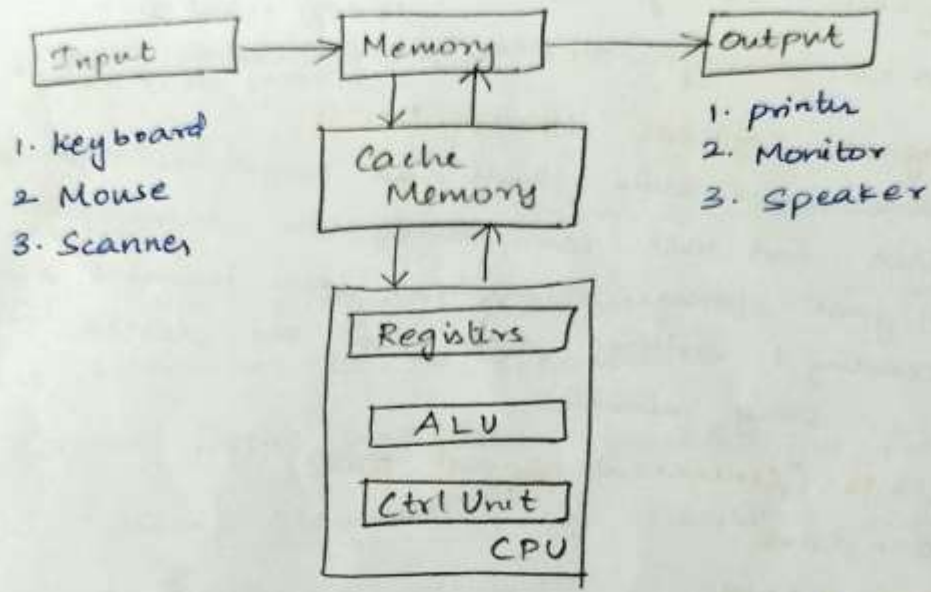
1. Mouse.
2. keyboard
3. microphone
4. Camera
5. Memory Card.

Hardware - Tangible components - run the instruction provided by the software.

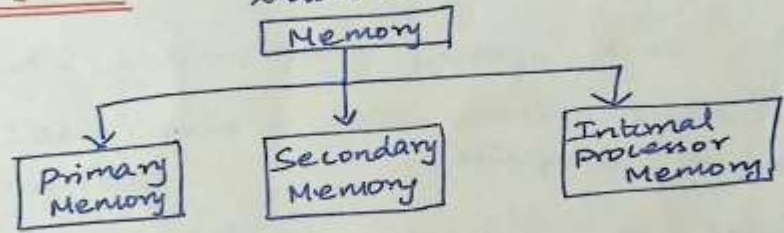
Software - Intangible components - User - H/w - interact & Command - Specific task.

- ① Application Software
- ② System Software.

# Basic Computer Organization.



② Memory Unit → Store data & instruction & intermediate processing results & Final processed information.



## Primary Memory.

- \* Built-in unit of the computer
- \* Data is stored in machine undecipherable binary form

### ① ROM (Read-only Memory) - (Non-volatile)

- permanent memory contents cannot be changed by end user.
- BIOS information - which performs POST (power-on self test).

### ② RAM - Volatile

- Temporary memory. power off contents get erased.
- Data is of no use, contents are erased.

### ③ Cache Memory

- Store data & related application that was last processed by CPU
- CPU & main memory - intermediate cache is placed

## Secondary memory.

\* External Storage device connected to computer.

\* Connected externally. Non-volatile.

① Magnetic storage device → read, erased & rewritten.  
Floppy, Hard disk

② Optical storage device → laser beam.  
CD-ROM, CD-RW, DVD

③ Magneto-optical storage device.

store information such as programs, files & backup data. End user can modify the information.

Higher storage capacity → laser beams & magnets for reading & writing data to the device.

Ex: Sony minidisc

④ USB (Universal Serial Bus)

pen drive.

Compactable.

Storage Capacity is large.

③ CPU

\* Brain of computer

\* processing in sly.

\* Controls the components of sly.

### Main operations

1. Fetching instructions from memory

2. Decode the inst to decide what operation to be done

3. Execute the instruction

4. Store the result in memory.

### Main Components:

1. ALU

2. CU

3. Registers.

1. ALU (Arithmetic Logic Unit)

- ↳ Arithmetic operations (add, sub, mul, div)
- ↳ Logical operations (AND, OR, NOT)

2. Control Unit (CU)

- Controls the flow of data & information
- CU uses program counter reg for fetching the next instruction to be execution.
- fetches instrn and gives to ALU for processing.
- CU uses Status register handling conditions such as overflow of data.

3. Registers.

- CPU - temporary storage unit - Registers.
- Data, instrn & intermediate results are stored in registers.

- (i) Program Counter (PC)
- (ii) Instruction Register (IR) - instrn to be decoded by CU
- (iii) Accumulator - results produced by CPU
- (iv) Mem. Add. Register (MAR) - Address of next. loc<sup>n</sup> in Mem. to be accessed
- (v) Mem. Buffer Register (MBR) - storing data sent/Received to CPU.
- (vi) Mem. Data Register (MDR) - Data + operands.

## Algorithm

\* Set of instruction when executed in a sequence will get a solution.

## Representation of Algorithm

1. Normal English
2. Flowchart
3. Pseudocode

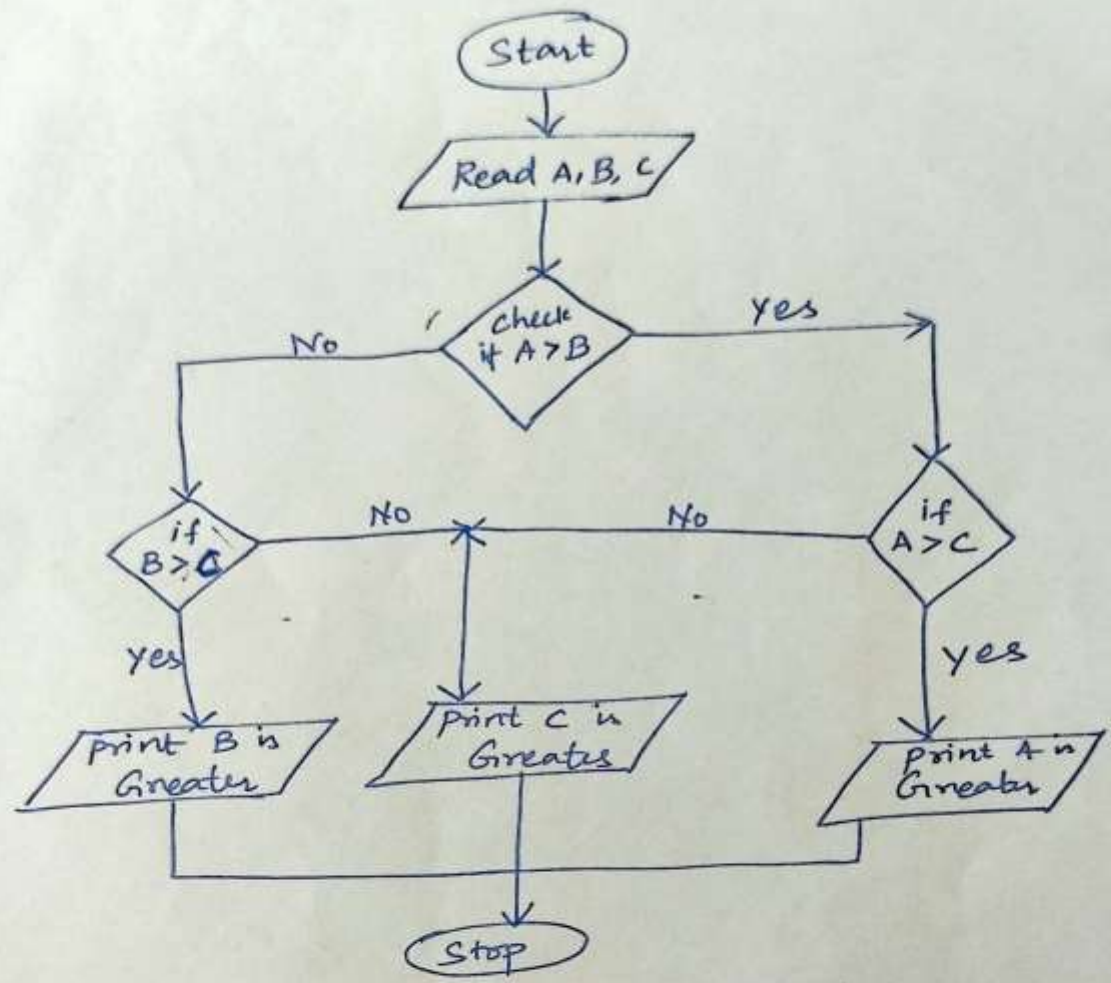
## Flowchart.

- ① Flow lines
- ② Terminal Symbols
- ③ Input/output Symbol
- ④ Process Symbol
- ⑤ Decision Symbol
- ⑥ Connectors.

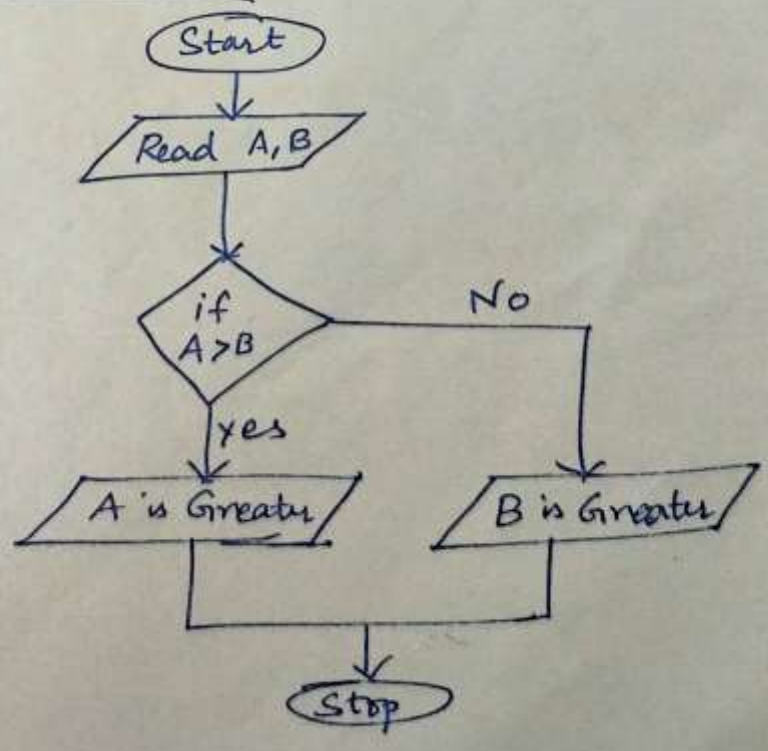
## Example:

1. Addition of two numbers
2. Greatest of two numbers, even/odd, Neg/positive
3. Calculate the SI, Area of Circle
4. Calculate Total & Average of Students in a class
5. Sum of 5 numbers.

# Greatest of Three Numbers.



# Greatest of two numbers.



\* **Algorithm**. After plan for developing pgm by logic - correct seq. & procedure of instr required to carryout the task.

\* **Algorithm** - logic of the program. It is the basic tool used to develop problem solving.

\* "a **Unambiguous - (clear)** sequence of instructions designed in such a way that if the instr are executed in specified sequence, the desired results will be obtained".

**Characteristics:**

- \* **Algorithm** - precise & unambiguous
  - instr should not be repeated again.
  - instr should be in sequence.
  - Result produced after algorithm terminates.

**Representation of Algorithms.**

- \* Normal English
- \* Decision table
- \* Flowchart
- \* program.
- \* Pseudocode

**Flowchart**

\* pictorial representation of an algorithm. Sequential steps are represented in flowchart using standard symbols.

\* layout & visual representations of the plan.

\* Symbol-oriented design - identifies the type of stmt from the symbols used.

\* Arrows ← Connecting b/w 2 symbols.

\* process of drawing flowchart for an algorithm is called Flowcharting.

\* **Flowcharts** - Easy to understand.

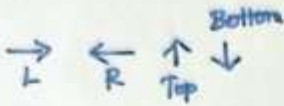
- helps in reviewing & debugging of a pgm.

- easy to analyze & compare various methods.



# Symbols used:

① Flowlines



Exact sequence in which instructions are to be executed. Drawn with arrow head.

② Terminal Symbol



[oval shape] → begin + end

→ start (no entering ↓)

→ stop (no ↓)

Exit flow line.

③ Input/output Symbol.

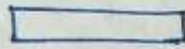


[parallelogram]

- Input (Read) & output (write)

- denotes fn of I/O devices in the pgm.

④ Process Symbol

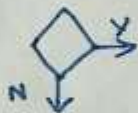


[Rectangle]

- used for calculations & initializations of memory locations.

- Arithmetic operations, data movements.

⑤ Decision Symbol



[Diamond shaped symbol]

- Indicate at a point decision is to be made b/w two alternatives.

- Yes (True), No (False).

⑥ Connectors



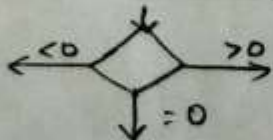
[circle & a digit/letter placed in link to specify the link].

Rules:

Example: Greatest of 2 nos, odd/even, +ve/-ve number.

\* standard symbols should be used.

\* only one should enter decision symbol, 2/3 flow lines can leave decision according to the possible answer.



\* Annotation Symbol

- describe data/computational steps more clearly

--- [This is top secret data]

Flow lines should not cross each other.

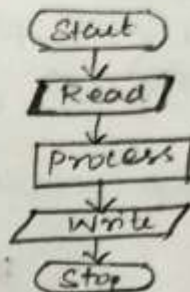
words in Flowchart - Common terms & easy to understand.

## Design structures in Flowcharts.

Design flowchart for any program & later we can combine to get the solution for complex problems. 3 designs:

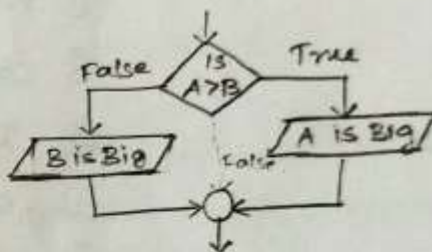
### ① Sequence structure.

- Simplest, series of steps
- Sequence (same direction)
- can include any number of instr.



### ② Selection structure.

- Decision (make questions).
- operations done on decision made.
- 2 exits (two branches)  
↓  
rejoined to single flow to exit the structure)
- Selection (True → one-sided selection)  
+ null-sided should end with Exit.

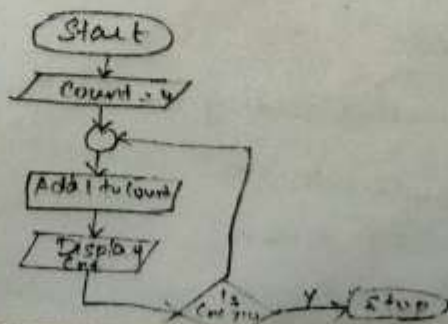


### ③ Loop structure.

Execute sequence of steps in no. of times until particular condition is met

(i) Top tested loop → if Count < 10  $\xrightarrow{T}$  goes into loop  
 $\xrightarrow{F}$  goes to start after loop.

(ii) Bottom tested loop → Trailing decision loop  
decision - last stmt of loop.



## Pseudocode.

- \* Flowcharting symbols were established by structured programming.
- \* So flowchart will not be able to handle some concepts.
- \* pseudocode - formal design tool with structured design
  - Visual, narrative, used for planning prog. logic

Pseudocode  
Also ↓ Called  
PDL  
Program Design  
Language

- pseudo (initiation / false).
- code (set of stmts / instr in pgm. lang).
- written in English, very clear.
- Rules:
  - ① Write one stmt per line.
  - ② Capitalize initial keywords.
  - ③ Indent to show hierarchy
  - ④ End multiline structure.
  - ⑤ keep stmt lang independent.

Example. To calculate student total and average.

READ name, class, M<sub>1</sub>, M<sub>2</sub>, M<sub>3</sub>

Total = M<sub>1</sub> + M<sub>2</sub> + M<sub>3</sub>

Average = Total / 3

IF average is greater than 75

Rank = Distinction

ENDIF

WRITE name, Total, Average, Rank

## Advantages.

- Not processor
- Easily Modified
- read & understood easily
- Converting pseudocode to pgm. lang is easy when compared with flowchart.

## Disadvantages.

- Not Visual (pictorial represent)
- No standardised style / format

## Computer Software

- \* Set of instructions grouped into programs that make the computer to function in the desired way.
- \* Collection of pgm to perform a task.
- \* instructs H/w what to do.
- \* pgm - set of instr <sup>written in high level lang</sup> <sup>undecodable by computer</sup> stored in memory  
CPU fetches an instr, decodes it & then executes required operation.  
After execution - pgm counter - next instr fetched & executed

## Types of Software.

- \* Application Software
- \* System Software.

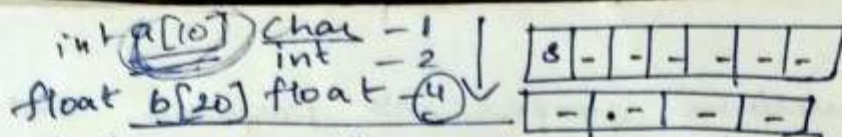
### ① System Software.

- \* Set of programs that governs the operation of computer system and make H/w to work.
- \* Controls the internal operations of computer sly.
- \* Types
  - OS - Controls all components (CPU, Mem, I/O devices) of computer. Ex: DOS, WINDOWS, UNIX, LINUX.
  - Language processors. Conversion of high level language to machine language.
    - ↳ Assembler
    - ↳ Interpreter
    - ↳ Compiler
  - Utility pgms - Virus Scanner, Disk defragmenter.

### ② Application Software

- \* Set of pgms to carry out operations of specific Appltn.
- \* It is written by programmers to enable computer to perform specific task.
- \* Types
  - Customised Appltn s/w - based on customer requirements.
  - General Appltn s/w - General requirements for a specific task. Customers can use it simultaneously.
- \* Example: Result preparation, Railway reservation, billing

**Notes:**



scanf("%f", &f)

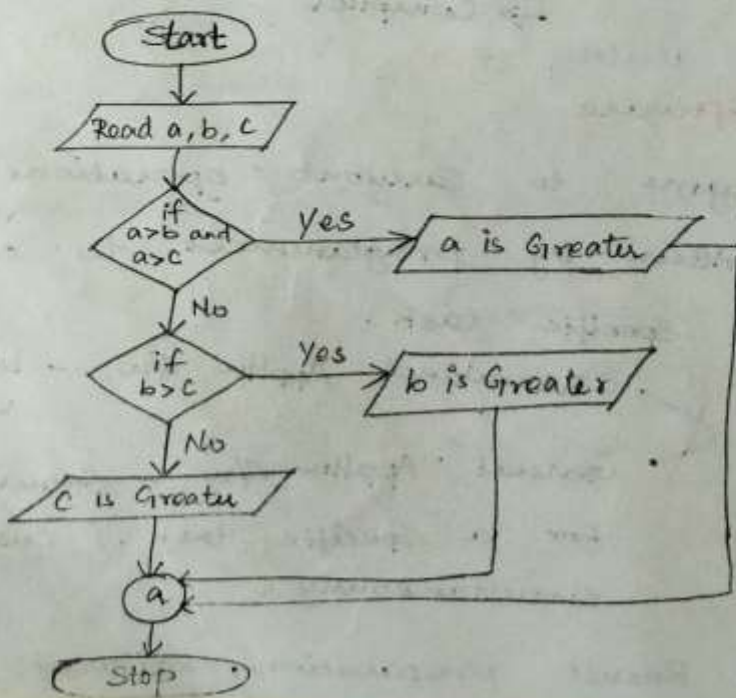
- \* OS - control all H/w parts of computer system
  - Manage resources & overall operations of computer system
- \* Function of OS - Memory Mgt
  - File Mgt
  - Security Mgt
  - Process Mgt
  - User Interface
- \* Compiler - Convert high-level lang to machine lang
  - Source code to binary code
- \* Interpreter - line-by-line executes the source code.
- \* Assembler - Convert Assembly lang to machine lang

**Examples:**

- ① Area of Circle.
- ② Convert Celsius to Fahrenheit.  $F = (1.8 \times C) + 32$ .
- ③ Greatest of 3 numbers.

Internet Terminologies

1. Web page
2. Home page
3. Browser
4. URL
5. ISP
6. Download & Upload
7. online & offline
8. Hypertext
9. Web server
10. Web site



$a > b$   
 $a > c$

a	b	c
5	10	15
15	10	5
5	15	10

# ALGORITHMIC PROBLEM SOLVING

Algorithmic problem solving is solving problem that require the formulation of an algorithm for the solution.

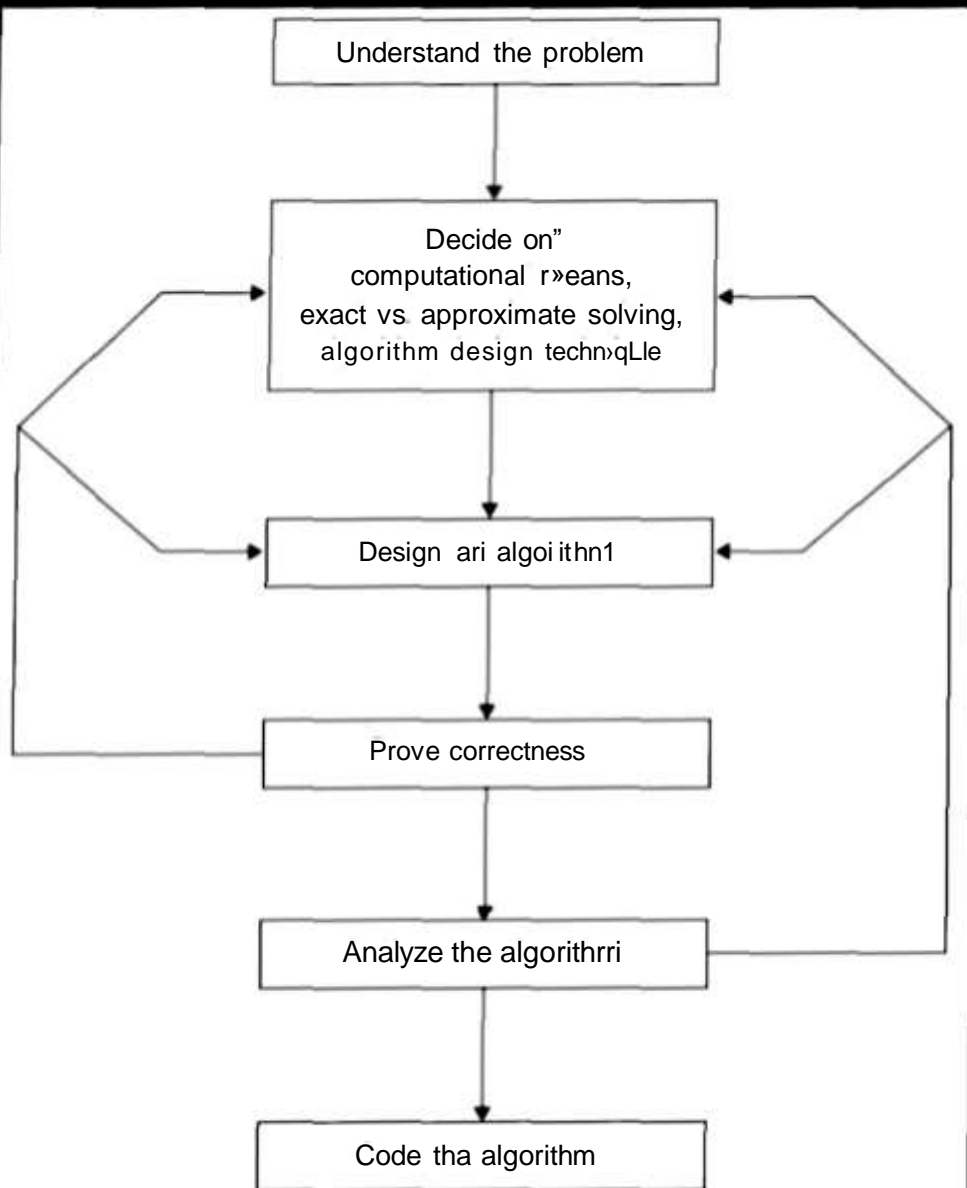


FIGURE 1.2 Algorithm design and analysis process.

## Understanding the Problem

- ❖ It is the process of finding the input of the problem that the algorithm solves.
- ❖ It is very important to specify exactly the set of inputs the algorithm needs to handle.

Understanding the Cap

p

## Approximate Problem Solving

• The next principal decision is to choose between solving the problem exactly or solving it approximately.

• Based on this, the algorithms are classified as

*exact algorithm and approximation algorithm. Example: exact is addition of numbers, approximation is solving linear equation*

### Deciding a data structure:

■ Data structure plays a vital role in designing and analysis the algorithms.

■ One of the algorithm design techniques also depend on the structuring data specifying a problem's instance

■ Algorithm+ Data structure=programs.



## Algorithm Design Techniques

❖ An *algorithm design technique* (or “strategy” or “paradigm”) is a general approach to solving problems algorithmically that is applicable to a variety of problems from different areas of computing.

## Methods of Specifying an Algorithm

❖ *Pseudocode, flowchart, programming language*

## Proving an Algorithm's Correctness

❖ Once an algorithm has been specified, you have to prove its *correctness*. That is, you have to prove that the algorithm yields a required result for every legitimate input in a finite amount of time.

❖ A common technique for proving correctness is to use mathematical induction because an algorithm's iterations provide a natural sequence of steps needed for such proofs.

## Analysing an Algorithm

*Efficiency.*

Time efficiency, indicating how fast the algorithm runs,

Space efficiency, indicating how much extra memory it uses.